

2/5 MESSAGE FORMAT BYTE: N-11 2 3 4 5 TO N - 2N 1 3 8 9 27 32 33 BIT: 16|17 24| 8x(N-2)**DATA CRC** P DA-**DATA** TA 0 TO 63 BYTES ID LGT Fig. 2A Fig. 2B BYTE:/ 2 3 **QRL ENTRY FORMAT** 89 16 17 BIT: 3 24 25 26 **REPORT** REPORT **MESSAGE** MSG. S **QUERY MESS-MESSAGE** INST. V **CLASS** AGE DATA ID **HEADER** C. NO. **FIELD** FROM POWER-ON **DATA** DA-**INTERRUPT** ID TA 50 LGT INITIALIZE RANDOM Fig. 4 ACCESS MEMORY FROM 500 µSEC. LOCATIONS **INTERRUPT** 53 60 SET INTERRUPT SET IR FOR REGISTER (IR) 500 μSEC. FOR 500 µSEC. INTERRUPT INTERRUPT 62 64 Fig. 3 DOES INTER-SAMPLE AND RE-NO 56 CORD VOLTAGE RUPT CONCLUDE AN INPUT BIT ON DATI LINE TO OPERATING **FUNCTIONS** YES 56

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